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In the claims:

Please cancel claims 1-18, 31 and 40.

1-18. Cancelled.

19. (Original) An ADSL POTS splitter including an enhanced low pass filter, the enhanced low pass filter, comprising:

- a first filter capacitor including a first lead and a second lead;

- a first non-isolated inductor including a first winding and a second winding;

- a second non-isolated inductor connected to the first non-isolated inductor and including a first winding and a second winding, the first winding of the second non-isolated inductor connected in series with the first winding of the first non-isolated inductor, the second winding of the second non-isolated inductor connected in series with the second winding of the first non-isolated inductor;

- a isolated inductor connected to the first non-isolated inductor and including a first winding and a second winding, the first winding of the isolated inductor connected in series with the first winding of the first non-isolated inductor and to the first lead of the first filter capacitor, the second winding of the isolated inductor connected in series with the second winding of the first non-isolated inductor and to the second lead of the first filter capacitor, wherein the first non-isolated inductor has a respective DC saturation current and the isolated inductor has a respective DC saturation current, the DC saturation current of the first non-isolated inductor being substantially greater than the DC saturation current of the isolated inductor for achieving higher inductance in the isolated inductor, the isolated inductor being essentially the same physical size as than the first non-isolated inductor, and

- a common mode choke connected between the first non-isolated inductor and the first filter capacitor, the common mode choke including a first winding and a second winding, the first winding of the common mode choke connected in series with the first winding of the first non-isolated inductor and with the first winding of the isolated inductor, the second

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winding of the common mode choke connected in series with the second winding of the first non-isolated inductor and with the second winding of the isolated inductor.

20. (Original) The enhanced low pass filter of claim 19 wherein the first non-isolated inductor and the second non-isolated inductor each have respective physical attributes and respective electrical attributes, said physical and electrical attributes of the first non-isolated inductor being essentially the same as said physical and electrical attributes of the second non-isolated inductor.

21. (Original) The enhanced low pass filter of claim 20 wherein said respective physical and electrical attributes of the first and the second non-isolated inductors include physical size and DC saturation current level, respectively.

22. (Original) The enhanced low pass filter of claim 20 wherein the isolated inductor has respective physical attributes and respective electrical attributes, said physical attributes of the first and the second non-isolated inductors being essentially the same as said physical attributes of the isolated inductor, said electrical attributes of the first and the second non-isolated inductors being substantially different than said electrical attributes of the isolated inductor.

23. (Original) The enhanced low pass filter of claim 22 wherein said respective physical and electrical attributes of the first non-isolated inductor, the second non-isolated inductor and the isolated inductor include physical size and DC saturation current level, respectively.

24. (Original) The enhanced low pass filter of claim 19, further comprising:

a second isolated inductor connected to the isolated inductor and including a first winding and a second winding, the first winding of the second isolated inductor connected in series with the first winding of the isolated inductor, the second winding of the second isolated inductor connected in series with the second winding of the isolated inductor.

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25. (Original) An ADSL POTS splitter including an enhanced low pass filter, the enhanced low pass filter, comprising:

- a first filter capacitor including a first lead and a second lead;

- a first non-isolated inductor including a first winding and a second winding;

- a second non-isolated inductor connected to the first non-isolated inductor and including a first winding and a second winding, the first winding of the second non-isolated inductor connected in series with the first winding of the first non-isolated inductor, the second winding of the second non-isolated inductor connected in series with the second winding of the first non-isolated inductor;

- a first isolated inductor connected to the first non-isolated inductor and including a first winding and a second winding, the first winding of the first isolated inductor connected in series with the first winding of the first non-isolated inductor and to the first lead of the first filter capacitor, the second winding of the first isolated inductor connected in series with the second winding of the first non-isolated inductor and to the second lead of the first filter capacitor, wherein the first non-isolated inductor has a respective DC saturation current and the first isolated inductor has a respective DC saturation current, the DC saturation current of the first non-isolated inductor being substantially greater than the DC saturation current of first isolated inductor for achieving higher inductance in the first isolated inductor, the first isolated inductor being essentially the same physical size as than the first non-isolated inductor;

- a second isolated inductor connected to the first isolated inductor and including a first winding and a second winding, the first winding of the second isolated inductor connected in series with the first winding of the first isolated inductor, the second winding of the second isolated inductor connected in series with the second winding of the first isolated inductor;

and

- a common mode choke connected between the first non-isolated inductor and the first filter capacitor, the common mode choke including a first winding and a second winding, the first winding of the common mode choke connected in series with the first winding of the

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first non-isolated inductor and with the first winding of the first isolated inductor, the second winding of the common mode choke connected in series with the second winding of the first non-isolated inductor and with the second winding of the first isolated inductor.

26. (Original) The enhanced low pass filter of claim 25 wherein said inductors each have respective physical attributes and respective electrical attributes, said physical and electrical attributes of each said inductor being essentially the same.

27. (Original) The enhanced low pass filter of claim 26 wherein said respective physical and electrical attributes of each said inductor include physical size and DC saturation current level, respectively.

28. (Original) The enhanced low pass filter of claim 25, further comprising:
an inductor damping resistor connected in parallel with each said winding of each said inductor.

29. (Original) The enhanced low pass filter of claim 28, further comprising:
a first attenuation pole tuning capacitor connected in parallel across the first windings of the first and the second isolated inductors; and
a second attenuation pole tuning capacitor connected in parallel across the second windings of the first and the second isolated inductors.

30. (Original) The enhanced low pass filter of claim 25 wherein:
each said inductor includes a respective dual section bobbin; and
each winding of each said inductor is wound on a respective section of the respective dual section bobbin.

31. Cancelled.

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32. (Original) A communication apparatus, comprising:

- a digital subscriber line access multiplexor;

- a central office ADSL transceiver unit electrically connected to the DSLAM; and

- a POTS splitter including an enhanced low pass filter, the enhanced low pass filter electrically connected to the central office ADSL transceiver unit and to a remote communication apparatus, the enhanced low pass filter comprising:

- a first filter capacitor including a first lead and a second lead;

- a first non-isolated inductor including a first winding and a second winding;

- a second non-isolated inductor connected to the first non-isolated inductor and including a first winding and a second winding, the first winding of the second non-isolated inductor connected in series with the first winding of the first non-isolated inductor, the second winding of the second non-isolated inductor connected in series with the second winding of the first non-isolated inductor;

- an isolated inductor connected to the first non-isolated inductor and including a first winding and a second winding, the first winding of the isolated inductor connected in series with the first winding of the first non-isolated inductor and to the first lead of the first filter capacitor, the second winding of the isolated inductor connected in series with the second winding of the first non-isolated inductor and to the second lead of the first filter capacitor, wherein the first non-isolated inductor has a respective DC saturation current and the isolated inductor has a respective DC saturation current, the DC saturation current of the first non-isolated inductor being substantially greater than the DC saturation current of the isolated inductor for achieving higher inductance in the isolated inductor, the isolated inductor being essentially the same physical size as than the first non-isolated inductor; and

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a common mode choke connected between the first non-isolated inductor and the first filter capacitor, the common mode choke including a first winding and a second winding, the first winding of the common mode choke connected in series with the first winding of the first non-isolated inductor and with the first winding of the isolated inductor, the second winding of the common mode choke connected in series with the second winding of the first non-isolated inductor and with the second winding of the isolated inductor.

33. (Original) The enhanced low pass filter of claim 32 wherein the common mode choke is a bifilar wound inductor.

34. (Original) The enhanced low pass filter of claim 33 wherein the first non-isolated inductor has a respective DC saturation current and the common mode choke has a respective DC saturation current, the DC saturation current of the first non-isolated inductor being substantially greater than the DC saturation current of the common mode choke.

35. (Original) The enhanced low pass filter of claim 34, further comprising:

an inductor damping resistor connected in parallel with each said winding of each said inductor; and

a common mode choke damping resistor connected in parallel with each winding of the common mode choke.

36. (Original) The enhanced low pass filter of claim 35, further comprising:

an attenuation pole tuning capacitor connected in parallel with each said winding of the isolated inductor.

37. (Original) The enhanced low pass filter of claim 32, further comprising:

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a second non-isolated inductor connected to the first non-isolated inductor and including a first winding and a second winding, the first winding of the second non-isolated inductor connected in series with the first winding of the first non-isolated inductor, the second winding of the second non-isolated inductor connected in series with the second winding of the first non-isolated inductor.

38. (Original) The enhanced low pass filter of claim 37 wherein the first non-isolated inductor and the second non-isolated inductor each have respective physical attributes and respective electrical attributes, said physical and electrical attributes of the first non-isolated inductor being essentially the same as said physical and electrical attributes of the second non-isolated inductor.

39. (Original) The enhanced low pass filter of claim 38 wherein said respective physical and electrical attributes of the first and the second non-isolated inductors include physical size and DC saturation current level, respectively.

40. Cancelled.

41. (Original) An ADSL system, comprising:

- a remote communication apparatus; and

- a central office communication apparatus including an enhanced low pass filter connected to the remote communication apparatus, the enhanced low pass filter comprising:

- a first filter capacitor including a first lead and a second lead;

- a first non-isolated inductor including a first winding and a second winding;

- a second non-isolated inductor connected to the first non-isolated inductor and including a first winding and a second winding, the first winding of the second non-isolated inductor connected in series with the first winding of the first non-isolated inductor, the second winding of the second non-isolated

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inductor connected in series with the second winding of the first non-isolated inductor.

an isolated inductor connected to the first non-isolated inductor and including a first winding and a second winding, the first winding of the isolated inductor connected in series with the first winding of the first non-isolated inductor and to the first lead of the first filter capacitor, the second winding of the isolated inductor connected in series with the second winding of the first non-isolated inductor and to the second lead of the first filter capacitor, wherein the first non-isolated inductor has a respective DC saturation current and the isolated inductor has a respective DC saturation current, the DC saturation current of the first non-isolated inductor being substantially greater than the DC saturation current of the isolated inductor for achieving higher inductance in the isolated inductor, the isolated inductor being essentially the same physical size as than the first non-isolated inductor; and

a common mode choke connected between the first non-isolated inductor and the first filter capacitor, the common mode choke including a first winding and a second winding, the first winding of the common mode choke connected in series with the first winding of the first non-isolated inductor and with the first winding of the isolated inductor, the second winding of the common mode choke connected in series with the second winding of the first non-isolated inductor and with the second winding of the isolated inductor.

42. (Original) The enhanced low pass filter of claim 41 wherein the common mode choke is a bifilar wound inductor.

43. (Original) The enhanced low pass filter of claim 42 wherein the first non-isolated inductor has a respective DC saturation current and the common mode choke has a respective DC

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saturation current, the DC saturation current of the first non-isolated inductor being substantially greater than the DC saturation current of the common mode choke.

44. (Original) The enhanced low pass filter of claim 41, further comprising:

an inductor damping resistor connected in parallel with each said winding of each said inductor, and

a common mode choke damping resistor connected in parallel with each winding of the common mode choke.

45. (Original) The enhanced low pass filter of claim 44, further comprising:

an attenuation pole tuning capacitor connected in parallel with each said winding of the isolated inductor.

46. (Original) The enhanced low pass filter of claim 41 wherein the first non-isolated inductor and the second non-isolated inductor each have respective physical attributes and respective electrical attributes, said physical and electrical attributes of the first non-isolated inductor being essentially the same as said physical and electrical attributes of the second non-isolated inductor.

47. (Original) The enhanced low pass filter of claim 46 wherein said respective physical and electrical attributes of the first and the second non-isolated inductors include physical size and DC saturation current level, respectively

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